



GP 2815
PATENT
GE04270

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Date: November 5, 1998
PASSLACK, Matthias et al.

Serial No.: 09/022,593 Examiner: D. Hardy

Filed: February 12, 1998 Group Art Unit: 2815

For: SELF-ALIGNED METAL-OXIDE-COMPOUND SEMICONDUCTOR
DEVICE AND METHOD OF FABRICATION

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING
DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST
CLASS MAIL IN AN ENVELOPE ADDRESSED TO:
COMMISSIONER OF PATENTS AND TRADEMARKS
WASHINGTON, D.C. 20231, ON: 11-5-98

Date of Deposit



MOTOROLA INC.
Name of Assignee
11-5-98

SIGNATURE DATE

RECEIVED
98 NOV 13 PM 2:22

TECHNOLOGY CENTER 2800

ELECTION AND RESPONSE TO RESTRICTION REQUIREMENT

Honorable Assistant Commissioner of Patents,
Washington, D.C. 20231

SIR:

In response to the Office Action dated October 29, 1998, please reconsider
the above-identified patent application in view of the election and remarks
presented hereinafter. An election of the claims of Group I, claims 1-19, is
herein made.

Restriction for examination purposes was required under 35 U.S.C. 121, as
between the following groups of claims:

I. Group I, claims 1-19, drawn to a semiconductor device, classified in
Class 257, Subclass 410+; and

II. Group II, claims 20-28, drawn to a method of making a semiconductor device, classified in Class 438, Subclass 301+.

As required by the Office Action, Applicants elect to prosecute the device claims of Group I, that is claims 1 - 19.

Respectfully submitted,
Matthias Passlack et al.



Gregory J. Gorrie
Attorney for Applicants
Reg. No. 36,530
Tel.: (602) 441-4207
Fax: (602) 441-5220

MOTOROLA, INC.
Intellectual Property Dept.
P. O. Box 10219
Scottsdale, Arizona 85271-0219